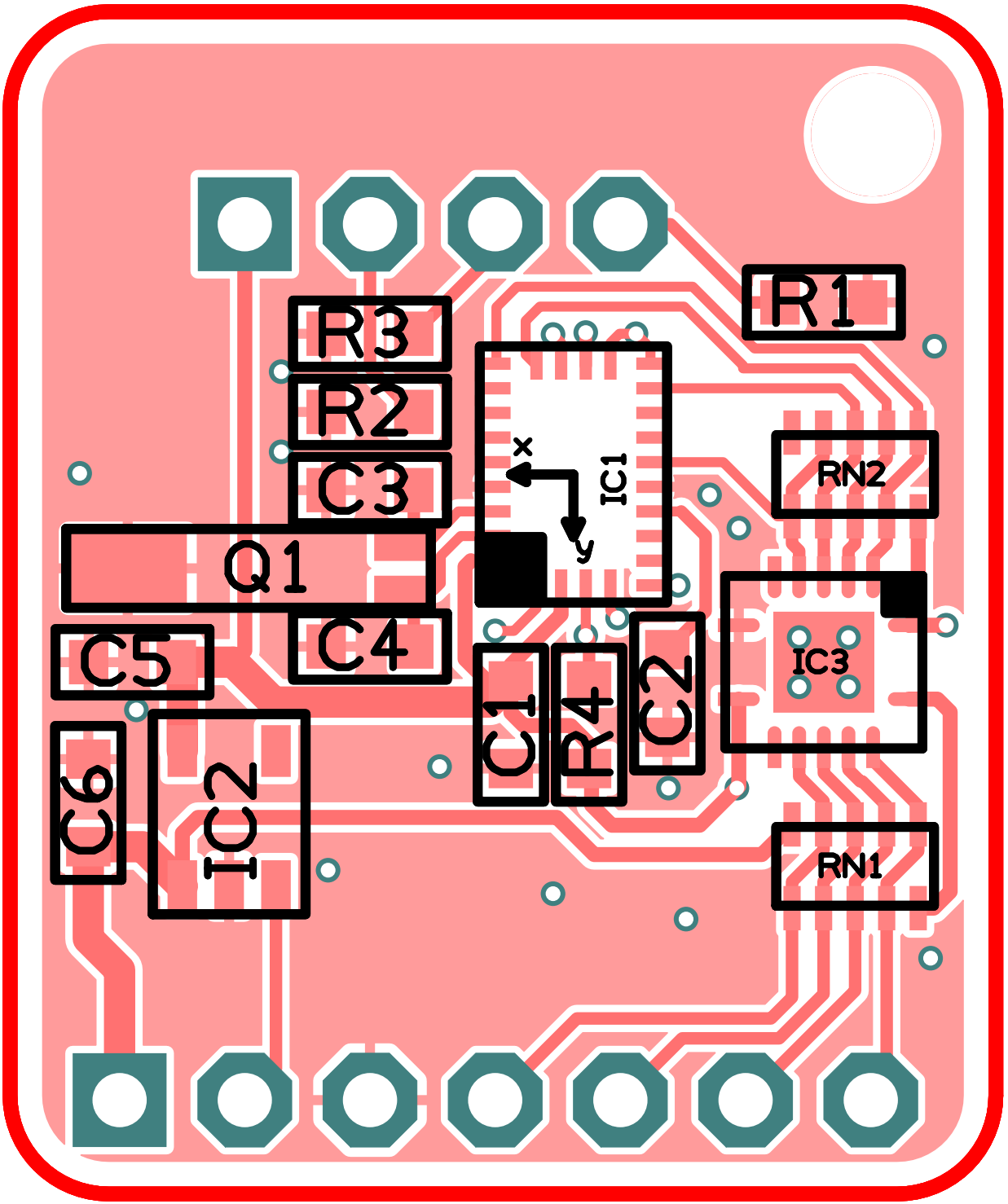
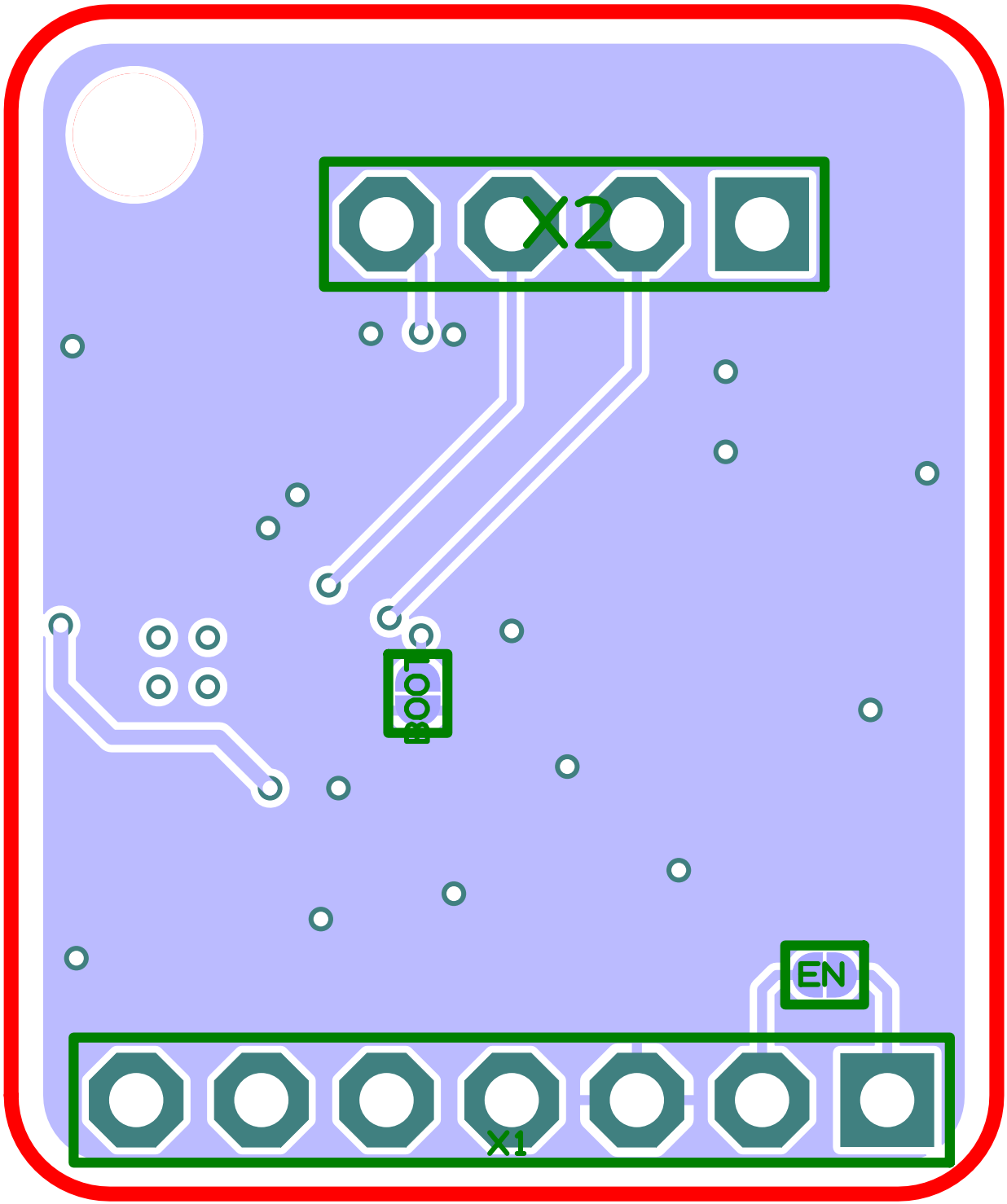

R1
R3
R2
C3
Q1
C4
C5
C6
IC2
IC1
x
y
C1
R4
C2
IC3
RN2
RN1


X2
BOOT
EN
X1